



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Richard Bullock

Serial No.: 09/773,872

Date: April 29, 2004

Filed: February 2, 2001

Group Art Unit: 2812

For: METHOD OF FABRICATING A GATE DIELECTRIC LAYER FOR A
THIN FILM TRANSISTOR

Mail Stop: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision dated October 29, 2003 of the Primary Examiner finally rejecting claims 1-19.

The Appeal Fee \$330 - Non-Small Entity is

☒ Enclosed

☐ Not required (fee paid in prior appeal)

☐ Charge to Deposit Account No. 15-0700

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. §1.136(a), to extend the time for filing a response by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700. In the event that the actual Appeal Fee is greater than the payment submitted herewith or is not enclosed, the Patent Office is authorized to charge the underpayment to Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop: AF Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, on April 29, 2004

Respectfully submitted,

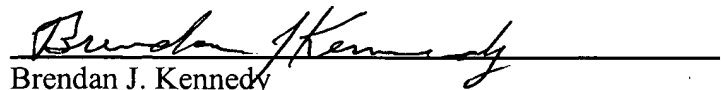
Brendan J. Kennedy

Name of applicant, assignee or
Registered Representative


Signature

April 29, 2004

Date of Signature


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Registration No.: 41,890

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